



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Tatsuya Arao et al.                      Art Unit : 2812  
Serial No. : 10/622,584                                  Examiner : Unknown  
Filed : July 21, 2003  
Title : METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Please correct the Filing Receipt for the above-referenced application to reflect the correct spelling of the name of inventor Tatsuya Arao.

Please supply a corrected Filing Receipt to the undersigned with respect to this application. A copy of the original Filing Receipt showing the desired changes in red ink is attached for your convenience.

No fee is believed to be due. If, however, there are any charges or credits, please apply them to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 11/10/03

  
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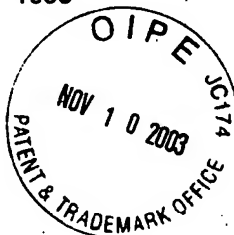


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
✓ 10/622,584	✓ 07/21/2003	2812	1068	12732-050002	✓ 25	✓ 33	✓ 4

26171  
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CONFIRMATION NO. 4703

## FILING RECEIPT



\*OC000000011082012\*

Date Mailed: 10/23/2003

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Applicant(s) Tatsuya

- ✓ Tasuya Arai, Kanagawa, JAPAN;
- ✓ Hideomi Suzawa, Kanagawa, JAPAN;
- ✓ Koji Ono, Kanagawa, JAPAN;
- ✓ Toru Takayama, Kanagawa, JAPAN;

## Assignment For Published Patent Application

- ✓ Semiconductor Energy Laboratory Co., Ltd., a Japan corporation;

## Domestic Priority data as claimed by applicant

- ✓ This application is a DIV of 09/873,334 06/05/2001 PAT 6,596,571

## Foreign Applications

- ✓ JAPAN 2000-170096 06/07/2000

If Required, Foreign Filing License Granted: 10/22/2003

Projected Publication Date: 01/29/2004

Non-Publication Request: No

Early Publication Request: No

No Docketing Required	
Reviewed By Practice Systems	Initials: <u>JP</u>
Reviewed By Billing Secretary	Initials: <u>CMT</u>

**Title**

Method of manufacturing semiconductor device

**Preliminary Class**

438

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